

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10595158	
	Filing Date		2006-03-09	
	First Named Inventor	Hajime Kimura		
	Art Unit	2629		
	Examiner Name	Jennifer Zubajlo		
Attorney Docket Number		12732-0325US1		

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	6556475	B2	2003-04-29	Yamazaki et al.	
	2	7015882	B2	2006-03-21	Yumoto	
	3	7256756	B2	2007-08-14	Abe	
	4	7796110	B2	2010-09-14	Abe	

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U.S.PATENT APPLICATION PUBLICATIONS

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	1	20060119552	A1	2006-06-08	Yumoto	

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FOREIGN PATENT DOCUMENTS

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1	05-062842	JP	B	1993-09-09		JP61-248604 is a laid-open publication corresponding to this reference	<input checked="" type="checkbox"/>
2	61-248604	JP	JP	1986-11-05		English abstract	<input checked="" type="checkbox"/>
3	2003-195812	JP	A	2003-07-09		English abstract and family to EP1286901, EP2148317, US7256756, US7796110	<input checked="" type="checkbox"/>
4	2003-195815	JP	A	2003-07-09		English abstract and family to EP1333422, US7015882, US2006/0119552	<input checked="" type="checkbox"/>
5	1 288 901	EP	A2	2003-03-05		In English	<input type="checkbox"/>
6	1 333 422	EP	A1	2003-08-06		In English	<input type="checkbox"/>
7	2 148 317	EP	A2	2010-01-27		In English	<input type="checkbox"/>

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NON-PATENT LITERATURE DOCUMENTS

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	1	OKAMURA, M, "Fig. 3-13 Circuit Example of Simplified Op-Amp," Standard Text OP-Amp Circuit Design, September 30, 1990, CQ Publishing Co., Ltd., p. 88 (4 pages with English translation)	<input checked="" type="checkbox"/>

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EXAMINER SIGNATURE

Examiner Signature		Date Considered	
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